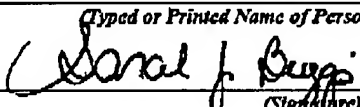


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Applicant(s): Robert B. Saethre et al.			2002-0042-01
Application No. 10/606,412	Filing Date June 25, 2003	Examiner Unassigned	Group Art Unit 2833
Invention: METHOD AND APPARATUS FOR ELECTRONICALLY INTERCONNECTING HIGH VOLTAGE MODULES ...			
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Robert B. Saethre et al.

U.S. Serial No.: 10/606,412

Filed: June 25, 2003

Title: METHOD AND APPARATUS FOR
ELECTRONICALLY
INTERCONNECTING HIGH VOLTAGE
MODULES POSITIONED IN
RELATIVELY CLOSE PROXIMITY

Examiner: Not Yet Assigned

Group Art Unit: 2833

STATUS INQUIRY

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

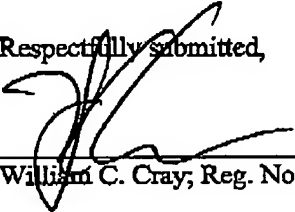
Sir:

Applicants hereby request an update of the present status of the above-identified patent application. Please note that this application was filed on June 25, 2003 and applicants have not yet received an Office Action for this application.

Conclusion

Applicants do not believe any fees are due in connection with this submission, however, if any fees are required, the Commissioner is authorized to charge any fees, or to credit any overpayment to Cymer, Inc.'s Deposit Account No. 03-4060.

Respectfully submitted,



William C. Cray, Reg. No. 27,627

August 24, 2004
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